IN THE UNITED STATES DISTRICT COURT
FOR THE NORTHERN DISTRICT OF ILLINOIS
EASTERN DIVISION

MAGNAVOX COMPANY, and )
SANDERS ASSOCIATES, INC., )
)
    Plaintiffs, )
)
    v. )
)
APF, ET AL., )
BALLY, ET AL., )
MONTGOMERY WARD, ET AL., )
K MART CORPORATION, )
and MATTEL, INC., ET AL. )
)
    Defendants. )

Judge George N. Leighton

Civil Action No. 77 C 3159
Civil Action No. 78 C 4951
Civil Action No. 78 C 5041
Civil Action No. 80 C 2409
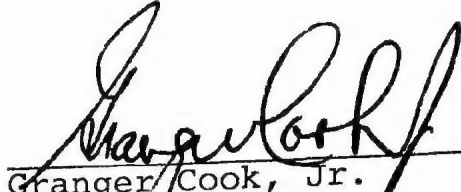Civil Action No. 80 C 4124

## NOTICE OF DEPOSITION

TO:   Theodore Anderson, Esquire
      James T. Williams, Esquire
      NEUMAN, WILLIAMS, ANDERSON & OLSON
      77 West Washington Street
      Chicago, Illinois  60602

      PLEASE TAKE NOTICE that Defendant Mattel, Inc. will

take the deposition of Mr. Garret Boer of the Lawrence Livermore

National Laboratory commencing at 10:00 A.M. on Tuesday, April 13,

1982 at Lawrence Livermore National Laboratory in Livermore,

California.  A subpoena duces tecum will be served requiring

the witness to produce those documents and things identified in Attachment A hereto.  You are invited to attend and cross examine if you so desire.

DATED:    March 8, 1982

_____
Granger Cook, Jr.
Edward D. Manzo
COOK, WETZEL & EGAN, LTD.
135 South LaSalle Street
Chicago, Illinois  60603
(312) 236-8500

Attorneys for MATTEL, ET AL.

OF COUNSEL:

Ronald M. Goldman, Esquire
MATTEL, INC.
5150 Rosecrans Avenue
Hawthorne, CA  90250

1. All documents related to the CC-30 installed at Lawrence Livermore National Laboratory including all manuals, communications, regarding its purchase or installation and wiring diagrams of it.

2. All documents relevant to the interface apparatus connecting the CC-301 controller to the computer system at Lawrence Livermore National Laboratory, including, specifically, the drawings by Mr. Kent Pryor connecting the CC-301 to a PDP-6 as identified in the following list:

| Date | Description | Number |
|---|---|---|
| 07/12/67 | Memory Address Register & Switches PDP-6 Console Display | LEA 67-3127 12-S O |
| 07/27/67 | Memory Buffer Shift Register, Bits 0-5 PDP-6 Console Display | LEA 67-3127 03-S O |
| 07/27/67 | Memory Buffer Shift Register, Bits 6-11 PDP-6 Console Display | LEA 67-3127 04-S O |
| 07/27/67 | Memory Buffer Shift Register, Bits 12-17 PDP-6 Console Display | LEA 67-3127 05-S O |
| 07/27/67 | Memory Buffer Shift Register, Bits 18-23 PDP-6 Console Display | LEA 67-3127 06-S O |
| 07/27/67 | Memory Buffer Shift Register, Bits 24-29 PDP-6 Console Display | LEA 67-3127 07-S O |
| 07/31/67 | Memory Buffer Shift Register, Bits 30-35 PDP-6 Console Display | LEA 67-3127 08-S O |
| 08/01/67 | Character Buffer & Parity Generator PDP-6 Console Display | LEA 67-3127 11-S A |
| 08/01/67 | Write Bits to Port Multiplexer PDP-6 Console Display | LEA 67-3127 09-S A |
| 08/29/67 | Word Counter & Special Character Control PDP 6 Console Display | LEA 67-3127 10-S 0 |
| 09/14/67 | PDP-6 Console Display Control, Part 2 | LEA 67-3127 02-S O |

| | | |
|---|---|---|
| 09/15/67 | PDP-6 Console Display<br>Control, Part 1 | LEA 67-3127 01-S A |
| 12/ /67 | Read Sequence - PDP-6 Console Display | LEA 67-3127 14-W O |
| 12/01/67 | Block Diagram<br>PDP-6 Console Display Interface | LEA 67-3127 13-B O |

3.   The CC-30 (without removing it from its current installation) in the computer room at Lawrence Livermore National Laboratory.